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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/814,306	04/01/2004	Katsuya Shinohara	56937-111	8591
7590	07/13/2007		EXAMINER	
McDERMOTT, WILL & EMERY 600 13th Street, N.W. Washington, DC 20005-3096			PATEL, SHAMBHAVI K	
			ART UNIT	PAPER NUMBER
			2128	
			MAIL DATE	DELIVERY MODE
			07/13/2007	PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/814,306	SHINOHARA, KATSUYA	
Examiner	Art Unit		
Shambhavi Patel	2128		

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

1)  Responsive to communication(s) filed on 26 April 2007.

2a)  This action is **FINAL**.                    2b)  This action is non-final.

3)  Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## **Disposition of Claims**

4)  Claim(s) 1-6 and 8 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
5)  Claim(s) \_\_\_\_\_ is/are allowed.  
6)  Claim(s) 1-6 and 8 is/are rejected.  
7)  Claim(s) \_\_\_\_\_ is/are objected to.  
8)  Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

9)  The specification is objected to by the Examiner.

10)  The drawing(s) filed on \_\_\_\_\_ is/are: a)  accepted or b)  objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)  The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a)  All b)  Some \* c)  None of:  
1.  Certified copies of the priority documents have been received.  
2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

1)  Notice of References Cited (PTO-892)  
2)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3)  Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_.

4)  Interview Summary (PTO-413)  
Paper No(s)/Mail Date \_\_\_\_.  
5)  Notice of Informal Patent Application  
6)  Other: \_\_\_\_.

**DETAILED ACTION**

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on **26 April 2007** has been entered.
2. Claims 1-6 and 8 have been presented for examination.

**Priority**

3. Acknowledgment is made of applicant's claim for foreign priority under 35 U.S.C. 119(a)-(d).

**Response to Arguments**

4. Applicant's arguments with respect to claims 1-6 and 8 have been considered but are moot in view of the new ground(s) of rejection.

**Claim Rejections - 35 USC § 112**

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

5. **Claim 6 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite** for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. The term "precise" is indefinite.

**Claim Rejections - 35 USC § 102**

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent,

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except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

6. **Claims 1-6 and 8 are rejected under 35 U.S.C. 102(e) as being clearly anticipated by Nightingale (US Pub. No. 2002/0152456).**

**Regarding claim 1:**

**Nightingale discloses a simulator apparatus comprising:**

- a. a plurality of simulator models each including a functional model workable as a CPU constituting a system to be simulated, peripheral hardware, or buses connecting the peripheral hardware to the CPU ([0053]-[0054]). The prior art discloses a software model (a smartcard driver) and a **hardware peripheral device** that is simulated using known hardware techniques. The hardware simulator contains a bus functional model ([0061] **BFM**).
- b. plural types of interfaces included in each of the simulator models and enabling the plural types of simulators for various uses to access to the functional models ([0057] “**the two interfaces...are the software interface...and the hardware interface**”).
- c. a simulator controlling device ([0055]) for selecting any of the plural types of interfaces and accessing the respective functional models via the selected interfaces ([0061]-[0062]). The hardware interface is accessed via the BFM and the software interface is accessed through the driver’s API.

**Regarding claim 2:**

**Nightingale discloses an interface using in a simulator for verifying software ([0062]).**

**Regarding claim 3:**

**Nightingale discloses an interface usable in a simulator for verifying hardware ([0061]).**

**Regarding claim 4:**

**Nightingale discloses an interface usable in a simulator for verifying a system ([0061]-[0062]; [0081]; [0089])**

**Regarding claim 5:**

**Nightingale discloses an interface usable in debugging ([0019]). The software and hardware interfaces can be used to ensure that the system is working correctly.**

**Claim Rejections - 35 USC § 103**

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of

each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

7. **Claims 6 and 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nightingale (US Pub. No. 2002/0152456) in view of Ghosh ('Methodology for Hardware/Software Co-Verification in C/C++).**

**Regarding claim 6:**

**Nightingale does not explicitly disclose** precise simulation for the system at clock level. As per the specification, this limitation is interpreted to mean that the output from the simulation correspond to the outputs of the pins. **Ghosh teaches** that the ports in the modules correspond to the hardware pins, and thus the output of the ports is tied to the output of the pins (**section 3.1.1 'Design of the BFM' 1<sup>st</sup> paragraph**). At the time of the invention, it would have been obvious to one of ordinary skill in the art to combine the teachings of Nightingale and Ghosh because the verification system of Ghosh provides great flexibility and enables faster simulation compared to existing methodologies (**Ghosh: abstract**).

**Regarding claim 8:**

**Nightingale does not explicitly disclose** an interface for performance analysis. **Ghosh teaches** a BFM that keeps track of the number of clock cycles used to perform various bus transactions and can provide a detailed report and the of the simulation (**Ghosh: section 3.1.6**). At the time of the invention, it would have been obvious to one of ordinary skill in the art to combine the teachings of Nightingale and Ghosh because the verification system of Ghosh provides great flexibility and enables faster simulation compared to existing methodologies (**Ghosh: abstract**).

**Conclusion**

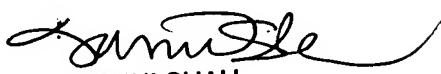
8. **Examiner's Remarks:** Examiner has cited particular columns and line numbers in the references applied to the claims above for the convenience of the applicant. Although the specified citations are representative of the teachings of the art and are applied to specific limitations within the individual claim, other passages and figures may apply as well. It is respectfully requested from the applicant in preparing responses, to fully consider the references in their entirety as potentially teaching all or part of the claimed invention, as well as the context of the passage as taught by the prior art or disclosed by the Examiner. In the case of amending the claimed invention, Applicant is respectfully requested to indicate the portion(s) of the specification which dictate(s) the structure relied on for proper interpretation and also to verify and ascertain the metes and bounds of the claimed invention.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Shambhavi Patel whose telephone number is (571) 272-5877. The examiner can normally be reached on Monday-Friday, 8:00 am – 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamini Shah can be reached on (571) 272-2279. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

SKP

  
KAMINI SHAH  
SUPERVISORY PATENT EXAMINER